

Att. Docket No. DE9-1999-0050US1
(590.018)

**RESPONSE UNDER 37 CFR 1.116
-- EXPEDITED PROCEDURE --
EXAMINING GROUP 2186**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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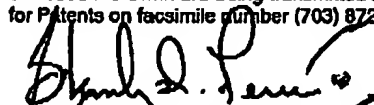
JUN 20 2005

In re Application of : Baier
Serial No. : 09/637,214 Examiner : D. Tran
Filed : August 11, 2000 Art Unit : 2186
For : METHOD AND SYSTEM FOR PROGRAMMING
FPGAs ON PC-CARDS WITHOUT ADDITIONAL
HARDWARE

June 20, 2005

AMENDMENT AFTER FINAL

I hereby certify that this correspondence and any documents referred to as enclosed therewith are being transmitted by facsimile to the Commissioner for Patents on facsimile number (703) 872-6306 on June 20, 2005.


Stanley D. Ference III
Reg. No. 33,879

June 20, 2005
Date of Signature

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Responsive to the Office Action dated April 20, 2005, kindly enter the following amendments. The amendments find full support in the specification as originally filed.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

*Enter upon
appeal
7/17/05
DT*